



SAINTGITS
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Office of the Controller of Examinations

SCE/COE/25/07/035

23rd July 2025

NOTIFICATION

It is hereby notified that, Third Semester B.Tech Degree Examinations (Supplementary – Special) June 2025, scheduled on 22.07.2025 is rescheduled as detailed below:

Course Code	Course Name	Scheduled Date	Rescheduled Date	Time of Examinations
20ECT203	Logic Circuit Design	22 nd July 2025 Tuesday	1 st August 2025, Friday	9.30 am - 12.30 pm
20CST203	Logic System Design	22 nd July 2025 Tuesday	1 st August 2025, Friday	9.30 am - 12.30 pm



Dr. Riboy Cheriyan
23/7/2025

Dr. Riboy Cheriyan
Controller of Examinations

Copy to:

1. Principal
2. Vice Principal, Deans & HoDs
3. Associate Dean (Academics & Compliance)
4. Examination Cell
5. Manager Office Administration
6. Help Desk
7. Website
8. Students B.Tech (2021 & 2022 Admissions) & SCE Faculty through Circular
9. Deputy Chief Warden - Hostel
10. PRO
11. Notice Board
12. Stock file

**SAINTGITS COLLEGE OF ENGINEERING
(AUTONOMOUS)**

Kottukulam Hills, Pathamuttom P. O.,
Kottayam – 686 532, Kerala
Tel: +91 481 2436169, 2436170
Extn: 150,155,156
www.saintgits.org