

**TIME TABLE**

Sub: Time schedule for the **End Semester Examination (Regular) November 2025 for Honours courses of Fifth and Seventh Semesters of B. Tech Programme (2020 scheme) – reg.**

Ref: Notification SCE/COE/25/11/012, Dated 7th November 2025

In continuation of the Notification read above, **Time Table** is published herewith for the information of all concerned.

Fifth Semester B.Tech. Degree Examination - Honours November 2025

Time of Examination: 9.30 am – 12.30 pm

SLOT	DATE	DAY	COURSE CODE	COURSE NAME
H	26th November 2025	Wednesday	20CST393	Neural Networks and Deep Learning
			20ECT391	FPGA Based System Design
			20EET397-A	Renewable Energy and Electric Vehicles
			23EVT391	Digital Logic Synthesis using HDL
			23ERT391	FPGA Based System Design

Seventh Semester B.Tech. Degree Examination - Honours November 2025

Time of Examination: 2.00 pm – 5.00 pm

SLOT	DATE	DAY	COURSE CODE	COURSE NAME
H	21 st November 2025	Friday	20CET491	Modern Construction Materials
			20CET493	Soil Dynamics and Machine Foundations
			20CST491	Cyber Forensics





			20CST493	Advanced Topics in Artificial Intelligence
			20CST497-B	Data Science using Python and R
			20ECT491	RF MEMS
			20EET491	Operation and Control of Generators
			20RBT491	Field Robotics



Dr. Riboy Cheriyan
 12/11/2025

Dr. Riboy Cheriyan
Controller of Examinations

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