

SCE/COE/26/03/062

27<sup>th</sup> March 2026

## TIMETABLE

### FOURTH SEMESTER B.TECH (2020 SCHEME) END SEMESTER EXAMINATION (SUPPLEMENTARY, FE) – APRIL 2026 FOR ELECTRONICS & COMPUTER ENGINEERING AND ELECTRONICS ENGINEERING (VLSI DESIGN AND TECHNOLOGY)

Ref: Notification No. SCE/COE/26/03/025, Dated 16<sup>th</sup> March 2026

In continuation of the Notification read as above, the detailed **Timetable for the Fourth Semester B.Tech. Degree End Semester Examination (Supplementary, FE)- April 2026** for **Electronics & Computer Engineering** and **Electronics Engineering (VLSI Design and Technology)** is published herewith.

#### Time of Examination

1.30 pm to 4.30 pm

1.30 pm to 3.30 pm (for 20MCN202 Constitution of India)

#### **ELECTRONICS & COMPUTER ENGINEERING**

SLOT	DATE	DAY	COURSE CODE	COURSE NAME
F	21 <sup>st</sup> April 2026	Tuesday	20MCN202	Constitution of India
E	23 <sup>rd</sup> April 2026	Thursday	20HUT200	Professional Ethics
B	28 <sup>th</sup> April 2026	Tuesday	20CST202	Computer Organization and Architecture
A	30 <sup>th</sup> April 2026	Thursday	23MAT202	Discrete Mathematics
C	05 <sup>th</sup> May 2026	Tuesday	23ERT202	Integrated Circuits
D	07 <sup>th</sup> May 2026	Thursday	23ERT204	Python for Machine Learning



**ELECTRONICS ENGINEERING (VLSI DESIGN AND TECHNOLOGY)**

SLOT	DATE	DAY	COURSE CODE	COURSE NAME
F	21 <sup>st</sup> April 2026	Tuesday	20MCN202	Constitution of India
E	23 <sup>rd</sup> April 2026	Thursday	20HUT200	Professional Ethics
B	28 <sup>th</sup> April 2026	Tuesday	23EVT204	Microcontrollers and Computer Architecture
A	30 <sup>th</sup> April 2026	Thursday	23EVT202	Analog Circuits
C	05 <sup>th</sup> May 2026	Tuesday	20ECT204	Signals and Systems
D	07 <sup>th</sup> May 2026	Thursday	23EVT206	Introduction to Microfabrication



*Riboy*  
27/3/2026

**Dr. Riboy Cheriyan**  
**Controller of Examinations**

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